Working group
Optimizing Real-Time Systems
(OVSTR)

Telecom ParisTech 26 April 2017
**Presentation and goals**

- **Topic:** optimizing the design of real-time systems from high-level language to hardware levels
  - Focusing on cross-layer optimizations

- **One day meetings**
  - Morning: invited talk (more) on the state-of-the-art
  - Afternoon: presentations focusing on a given subject

- **Goal:** try to define scientific problems to address between participants
  - A slot of 1 hour for discussions and proposals of topics for the next meeting
Program: modelling of continuous and logical

- 9h45 - 10h00 : welcome & goals
- 10h00 - 11h00 : Mixing Discrete and Continuous-time with the Synchronous Language Zelus - Marc Pouzet (ENS)
- 11h00 - 12h00 : Model-Based System Engineering for Cyber-Physical Systems - Frédéric Mallet (INRIA)
- 12h00 - 13h30 : lunch break
- 13h30 - 14h00 : Logical Time for Behavioral Semantics in the Gemoc Studio - Julien Deantoni (INRIA)
- 14h00 - 14h30 : Génération de code multi-cibles pour optimiser les ressources matérielles - Daniel Martins (Mathworks)
- 14h30 - 15h00 : Recent developments for mono- or multirate parallel real-time co-simulation: extrapolation and scheduling for multicore architectures - Nicolas Pernet (IFPEN)
- 15h00 - 15h30 : Qualitative modeling and simulation for the analysis of complex hybrid systems. Jean-Pierre Gallois (CEA)
- 15h30 - 16h00 : coffee break
- 16h00 - 17h00 : discussions and proposals of topics for the next meeting.