

FROM RESEARCH TO INDUSTRY

cea tech



Working group Optimizing Real-Time Systems (OVSTR)



Telecom ParisTech 15th October 2015

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leti & list

- Topic: optimizing the design of real-time systems from high-level language to hardware levels
 - Focusing on cross-layer optimizations
- Up to 3 meetings per year ... (will see for 2015)
 - Morning: invited talk (more) on the state-of-the-art
 - Afternoon: presentations focusing on a given subject
- Goal: try to define scientific problems to address between participants
 - A slot of 1 hour for discussions and proposals of topics for the next meeting

- 10h00 - 11h00: CRPD issues when computing WCETs (Jan Reineke)
- 11h00 - 12h00: CRPD scheduling problems (Pascal Richard)
- 12h00 - 13h30: lunch break
- 13h30 - 14h00: Time-predictable (stack) caches and their analysis (Florian Brandner)
- 14h00 - 14h30: Measurement-bases WCET estimation: the worst-case is a rare event (Liliana Cucu-Grosjean)
- 14h30 - 15h00: CRPD and priority assignment (N. Tran Hai)
- 15h00 - 15h30: MultiCore verification by integrating static timing Analysis with scheduling analysis (Sebastian Altmeyer)
- 15h30 - 16h00: coffee break
- 16h00 - 17h00: discussions and proposals for the topic of the next meeting